

Cambridge O Level

COMPUTER SCIENCE

Paper 1 MARK SCHEME Maximum Mark: 75 2210/11 May/June 2020

Published

Students did not sit exam papers in the June 2020 series due to the Covid-19 global pandemic.

This mark scheme is published to support teachers and students and should be read together with the question paper. It shows the requirements of the exam. The answer column of the mark scheme shows the proposed basis on which Examiners would award marks for this exam. Where appropriate, this column also provides the most likely acceptable alternative responses expected from students. Examiners usually review the mark scheme after they have seen student responses and update the mark scheme if appropriate. In the June series, Examiners were unable to consider the acceptability of alternative responses, as there were no student responses to consider.

Mark schemes should usually be read together with the Principal Examiner Report for Teachers. However, because students did not sit exam papers, there is no Principal Examiner Report for Teachers for the June 2020 series.

Cambridge International will not enter into discussions about these mark schemes.

Cambridge International is publishing the mark schemes for the June 2020 series for most Cambridge IGCSE[™] and Cambridge International A & AS Level components, and some Cambridge O Level components.

Generic Marking Principles

These general marking principles must be applied by all examiners when marking candidate answers. They should be applied alongside the specific content of the mark scheme or generic level descriptors for a question. Each question paper and mark scheme will also comply with these marking principles.

GENERIC MARKING PRINCIPLE 1:

Marks must be awarded in line with:

- the specific content of the mark scheme or the generic level descriptors for the question
- the specific skills defined in the mark scheme or in the generic level descriptors for the question
- the standard of response required by a candidate as exemplified by the standardisation scripts.

GENERIC MARKING PRINCIPLE 2:

Marks awarded are always **whole marks** (not half marks, or other fractions).

GENERIC MARKING PRINCIPLE 3:

Marks must be awarded **positively**:

- marks are awarded for correct/valid answers, as defined in the mark scheme. However, credit is given for valid answers which go beyond the scope of the syllabus and mark scheme, referring to your Team Leader as appropriate
- marks are awarded when candidates clearly demonstrate what they know and can do
- marks are not deducted for errors
- marks are not deducted for omissions
- answers should only be judged on the quality of spelling, punctuation and grammar when these features are specifically assessed by the question as indicated by the mark scheme. The meaning, however, should be unambiguous.

GENERIC MARKING PRINCIPLE 4:

Rules must be applied consistently e.g. in situations where candidates have not followed instructions or in the application of generic level descriptors.

GENERIC MARKING PRINCIPLE 5:

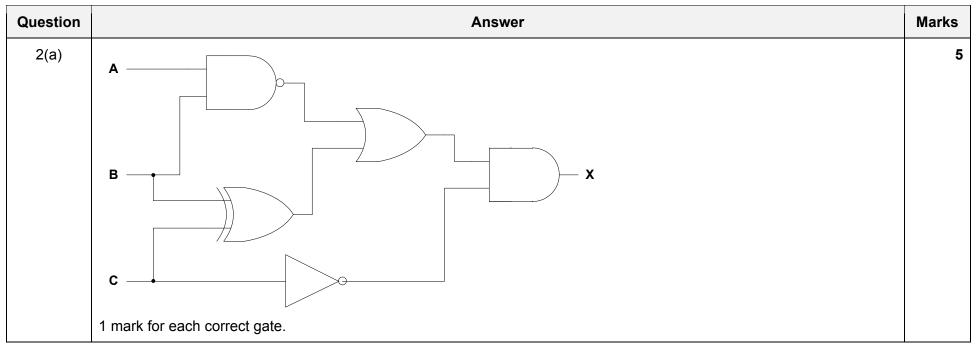
Marks should be awarded using the full range of marks defined in the mark scheme for the question (however; the use of the full mark range may be limited according to the quality of the candidate responses seen).

GENERIC MARKING PRINCIPLE 6:

Marks awarded are based solely on the requirements as defined in the mark scheme. Marks should not be awarded with grade thresholds or grade descriptors in mind.

Question	Answer	Marks
1(a)	Any one from: – Microphone – Touchscreen – Camera – Button	1
1(b)	Any two from: – Speaker – Touchscreen – Light/flash	2
1(c)(i)	Any one from: – Media access control – Unique address given to each device	1
1(c)(ii)	Any three from: - Uses hexadecimal values - Normally 48/64 bits in length (accept any other reasonable value) - First half is manufacturer number/code/ID - Second half is serial number	3
1(d)	 It needs RAM to store the data and programs currently in use It needs ROM to permanently store the boot up instructions 	2
1(e)(i)	Any two from: - Fingerprint scanner - Voice recognition - Retina/iris recognition - Facial recognition	2
1(e)(ii)	Any two from: - Adds extra level of security - Biometric device requires properties unique to individual - Allows quicker access as no need to input password // don't need to remember password	2

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Question				Answer		Marks
2(b)	Α	В	С	Working space	x]
	0	0	0		1	
	0	0	1		0	
	0	1	0		1	
	0	1	1		0	
	1	0	0		1	
	1	0	1		0	
	1	1	0		1	
	1	1	1		0	
	4 marks for 8 correct outpu 3 marks for 6 or 7 correct of 2 marks for 4 or 5 correct of 1 mark for 2 or 3 correct of	outputs outputs		<u></u>		-

Question	Answer	Marks
3(a)	 Any three from: It is a universal standard It can't be inserted the wrong way around Supports different transmission speeds Automatically detects if correct driver installed It will charge the mobile device at the same time 	3

Question	Answer	Marks
3(b)	– Serial	1
3(c)	 A compression algorithm is used No data is removed in the compression process An index/dictionary of pixels is created The number of times a pixel is repeated in a row is stored 	4

Question	Answer	Marks
4(a)	 Any two from: They both calculate a value from the data They both append the calculated value to the data They both recalculate the value They both report an error if they don't match 	2
4(b)	 One mark for method, three marks for description: Automatic Repeat reQuest Uses acknowledgement / request and time-out Error control protocol Check performed on receiving data // error is detected by e.g. parity check, check sum If error detected, request is sent to resend data // negative acknowledgement is used Resend request is repeated till data is sent correctly / requests timeout / limit is reached Send acknowledgement that data is received // positive acknowledgement is used 	4
	Parity Check - A parity bit is added (to the parity byte) - Counts / checks number of 1's - Can be even or odd - If parity is incorrect, error is detected	

Question	Ansv	ver		Marks
5	Component	CPU component (✓)	Not a CPU component (\checkmark)	6
	Arithmetic logic unit (ALU)	×		
	Hard disk drive (HDD)		✓	
	Memory address register (MAR)	×		
	Random access memory (RAM)		~	
	Solid state drive (SSD)		~	
	Control unit (CU)	✓		

Marks	Answer			
4)		Sensor	6
	oaching an automatic	Detecting wh door system	Pressure / motion / infra-red	
	a river	Monitoring th	pH / light	
	s 25 degrees Celsius	Checking if a	Temperature	
	t cross a bridge	Counting the	Magnetic field / pressure / motion / infra-red	
		Counting the		On

Question	Answer	Marks
7(a)(i)	 Any four from: Keylogger is downloaded without knowledge (by example) Keylogger records key presses Data is relayed back to third party Data is analysed // Patterns in data could reveal log-in details details can then be used to log into the account 	4
7(a)(ii)	Any one from: - Use drop-down boxes for password - Two-step verification (by example) - Partial password requests - Onscreen / virtual keyboard	1
7(b)(i)	Any one from: – Look for locked padlock / green padlock – Check for https	1
7(b)(ii)	 Any four from: requests web server to identify itself // request to view the (SSL) certificate receives a copy of the (SSL) certificate, sent from the webserver checks if (SSL) certificate is authentic/trustworthy sends signal back to webserver that the certificate is authentic/trustworthy 	4

Question		Answer			Marks
8(a)	– Interru	upt			1
8(b)	Benefit: – Printir – Can u – Can p – No wa Drawbacks: – Printir – Ink is	penefit, two marks for drawbacks ng may be higher quality use larger paper sizes print onto different media arm-up time ng will be slower more expensive per page un be smeared // ink is not smudge proof			3
8(c)		Statement	Inkjet (√)	Laser (√)	4
		Uses a rotating drum to transfer the image to the paper		~	
		Uses powdered toner		~	
		Uses nozzles to spray droplets on to the paper	~		
		Uses a print head mechanism that moves side to side	~		
	One mark per	each correct row			

Question	Answer	Marks
9(a)	 Any three from: Closer to/is machine code May use mnemonics May need an assembler to be translated One line of code represents a single instruction Machine dependent Have direct access to memory locations/registers 	3
9(b)	 Assembly code Machine code 	2
9(c)	Any one from: - It is more difficult to understand - Error prone - Have to manipulate memory locations - Machine dependent	1
9(d)(i)	- 10110100 - 11001001	2
9(d)(ii)	 000011111010 (must have leading zeros) 	1
9(d)(iii)	- 93 - 1D	2